

# Abstracts

## A 30 GHz Monolithic Receiver

---

*L.C.T. Liu, C.S. Liu, J.R. Kessler and S.K. Wang. "A 30 GHz Monolithic Receiver." 1986 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 86.1 (1986 [MCS]): 41-44.*

Several monolithic integrated circuits have been developed to make a 30 GHz receiver. The LNA chip has 7 dB noise figure with 14 dB gain. The IF amplifier has 13 dB gain with 30 dB control range. The mixer and phase shifter have conversion loss and insertion loss of 10.5 dB and 1.6 dB, respectively.

 [Return to main document.](#)